

# Gaurav Deshmukh

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## Education

**University of California, Irvine**

Irvine, CA | June 2022

B.S. in Computer Science with Specialization in Artificial Intelligence, GPA: 3.6

Minor in Management

## Experience

**Intel Corporation** | *FPGA Solutions Engineer*

Santa Clara, CA | Aug 2022 – Aug 2025

- Built and maintained internal software tools supporting hybrid FPGA client/server chip verification workflows, accelerating validation cycles across 9+ silicon and IP programs
- Integrated innovative transactor tech for NPU (Neural Processing Unit) prototyping in next-gen architectures
- Developed internal automation scripts (Python, SQL) to streamline log ingestion and reduce manual validation overhead
- Designed and deployed Kubernetes-hosted dashboarding systems used by 3+ engineering teams for build, debug, and performance tracking

**Intel Corporation** | *Graduate Technical Intern*

Santa Clara, CA | Jun 2021 – Oct 2021

- Designed and developed an interactive dashboard through Grafana to provide FPGA Engineers an easy-to-use interface to view and analyze data generated from FPGA build, run, and debug processes
- Implemented framework for future predictive analytics through a Keras deep-learning regression model
- Established an automated and secure flow of data from Intel log databases to the dashboarding tool through an intermediary PostGreSQL database
- Worked with FPGA Solutions team to provide multi-threaded support for future project developments

**Siemens EDA** | *Data Analyst Intern*

Fremont, CA | Jun 2020 – Sep 2020

- Contributed to predictive analytics and machine learning on data generated from emulation runs to provide customers with better optimization options, leading to 45% less power usage
- Developed a Python-based ETL to consume C++ data structures
- Worked with databases in SQL for structuring, merging, and organizing raw emulation data

## Projects

**FYM Technologies** | *Data & Systems Engineer*

September 2025 – Present

- Work trackside with 3 race teams and mechanics to provide widespread support on all data and electrical related systems on Toyota GT4, Ferrari Challenge, Porsche Cup, and Ferrari GT3 cars
- Gather and analyze telemetry data during each race session to optimize driver and car performance
- Support ECU integration and electrical data systems troubleshooting
- Develop and refine ML regression models that convert race telemetry data into actionable strategy and driver performance insights

**AI-Driven Yellow Flag Predictor**

Jan 2021 – Present

- Developed a neural net binary classifier using Keras deep learning to generate yellow flag predictions
- Use Selenium to scrape live timing data from IMSA and stored within a MongoDB
- Continually used for IMSA events by teams partnered with FYM Technologies

## Skills & Interests

**Languages:** Python, C++, Java, SQL, Swift

**Interests:** Automotive technology & motorsports, AI in everyday and creative applications, Soccer, Golf, Basketball, Cryptocurrency

**Frameworks & Libraries:** TensorFlow, Keras, Scikit-learn, OpenCV

**Tools:** Kubernetes, Docker, Grafana, MongoDB, PostgreSQL